vxWorks-Front-Ends - Issues

#	Tracker	Status	Priority	Subject	Assignee	Updated
8133	Bug	New	Normal	Bus errors occur in a cycle following a cycle of the same type which is longer the normal	Kevin Martin	03/20/2015 10:20 AM
11711	Bug	Closed	High	Large bus errors are sometimes seen during the super cycle after the time line has been changed.	Kevin Martin	06/13/2016 03:06 PM
8134	Bug	Closed	High	MECAR periodically hard crashes	Kevin Martin	10/22/2015 11:47 AM
7284	Bug	Closed	Normal	MECAR regulation problem upon attempted turn on after a power outage	Kevin Martin	03/13/2015 09:38 AM
7192	Bug	Closed	High	MECAR won't turn on due to a Bypass Loop trip	Kevin Martin	11/05/2014 10:59 AM
7529	Bug	Closed	Normal	Special One-Shot Timelines cause Super Cycle learning to clear all Learning buffers.	Kevin Martin	03/17/2015 02:07 PM
12917	Bug	Closed	High	The mi_beam_intensity MDAT frame is unsigned. However, I've been treating it as if it was signed.	Kevin Martin	11/10/2017 10:36 AM