ANNIE1-electronics-design - Issues

#	Tracker	Status	Priority	Subject	Assignee	Updated
11351	Task	Assigned	Normal	DAQ output TOOL	Benjamin Richards	01/15/2016 08:03 AM
11349	Task	New	High	DAQ board reader TOOL		01/15/2016 08:05 AM
11348	Task	New	Normal	Postgress setup		01/05/2016 08:22 AM
11347	Task	New	Normal	DAQ monitoring system	Benjamin Richards	01/05/2016 08:21 AM
11346	Task	Resolved	Normal	DAQ Control systems	Benjamin Richards	01/15/2016 08:07 AM
11345	Task	New	Normal	DAQ Serialisation		01/05/2016 08:17 AM
11344	Task	Resolved	Normal	DAQ Jason (lite) messgae passing	Benjamin Richards	01/15/2016 08:09 AM
11343	Task	Resolved	Normal	DAQ Bool functions /status reporting	Benjamin Richards	01/15/2016 08:08 AM
11342	Task	Resolved	Normal	DAQ ZMQ/multicast integration	Benjamin Richards	01/15/2016 08:07 AM
10543	Task	Resolved	Normal	Check availability of local timing references	Jonathan Eisch	11/02/2015 01:31 PM
10535	Task	New	Normal	Select and buy a suitable GPS receiver	Jonathan Eisch	01/25/2018 07:16 AM
10534	Task	Assigned	Normal	Determine GPS antenna location at ANNIE Hall	Jonathan Eisch	01/25/2018 07:17 AM
10533	Task	Assigned	Normal	Determine the suitability of GPS timing reference	Jonathan Eisch	01/25/2018 07:17 AM
10532	Task	Assigned	Normal	Select timing sources	Jonathan Eisch	01/25/2018 07:17 AM
10530	Task	Assigned	Normal	Timing distribution system	Jonathan Eisch	01/25/2018 07:17 AM
10512	Task	New	Normal	Test final pickoff/splitter		04/06/2016 11:14 PM
10510	Task	Assigned	Normal	Mount pickoff/splitter pcb in enclosure, solder wires to bulkhead connectors	Jonathan Eisch	04/06/2016 11:09 PM
10509	Task	Assigned	Normal	Attach bulkhead connectors and pcb mounting hardware	Jonathan Eisch	04/06/2016 11:13 PM
10470	Task	Resolved	Normal	Machine pickoff/splitter enclosure	Jonathan Eisch	01/12/2016 03:05 PM
10468	Task	Assigned	Normal	Pickoff/splitter enclosure production	Jonathan Eisch	04/06/2016 11:13 PM
10463	Task	Assigned	Normal	HV pickoff/splitter enclosure	Jonathan Eisch	04/06/2016 11:13 PM
10461	Task	Resolved	Normal	Full sized pickoff/splitter board population	Jonathan Eisch	01/12/2016 03:07 PM
10457	Task	Assigned	Normal	Design and build full sized pickoff/splitter board	Jonathan Eisch	01/12/2016 03:07 PM
10456	Task	Resolved	Normal	Test HV resilliance up to 3kV	Jonathan Eisch	10/12/2015 02:40 PM
10449	Task	Assigned	Normal	HV pickoff & signal splitter	Jonathan Eisch	04/06/2016 11:14 PM
10443	Task	Assigned	Normal	Make readout methods (to be used by artdaq)	Jonathan Eisch	01/25/2018 07:16 AM
10442	Task	Resolved	Normal	Throughput testing for ADC cards	Jonathan Eisch	10/22/2015 01:40 PM
10440	Task	Assigned	Normal	Externally trigger ADC cards	Jonathan Eisch	01/25/2018 07:14 AM
9090	Feature	Assigned	Normal	UC ADC user manual	Jonathan Eisch	01/25/2018 07:14 AM
9089	Task	Assigned	Normal	UC ADC cards	Jonathan Eisch	01/25/2018 07:16 AM
9086	Task	Assigned	Normal	Contact Zarko Pavlovic about beam timing signals.	Jonathan Eisch	06/10/2015 10:48 AM

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8913	Task	Assigned	Normal	Send an ACDC + central card to QMUL	Matthew Wetstein	10/09/2015 01:09 PM
8816	Milestone	New	Normal	What is our calibration system?		01/25/2018 07:13 AM
8815	Task	New	Normal	Final Decision on MRD and veto electronics		04/06/2016 11:08 PM
8813	Task	Assigned	Normal	Design for clock distribution and timing	Jonathan Eisch	01/25/2018 07:15 AM
8812	Task	Assigned	Normal	Solidify Layout and number of crates channels for ANNIE RunI (Run II)	Matthew Wetstein	04/06/2016 11:14 PM
8811	Task	Resolved	Normal	Figure out the data rates and size of output from the system	Jonathan Eisch	10/22/2015 01:38 PM
8809	Task	Assigned	Normal	Interface for PSEC electronics with ART system	Karen Hayrapetyan	10/09/2015 01:06 PM
8808	Task	New	Normal	Get details of what we can get from the beam spill and timing	Jonathan Eisch	06/10/2015 10:48 AM
8806	Milestone	New	Normal	Redesign of LAPPD central card	Matthew Wetstein	10/09/2015 02:12 PM