Instrumentation Drivers - Issues

#	Tracker	Status	Priority	Subject	Assignee	Updated
13457	Bug	Resolved	Normal	80MHz digitizer driver should sanity check Nchannels setting	Dennis Nicklaus	08/05/2016 03:03 PM
13456	Bug	Resolved	Normal	80MHz Digitizer requires repeating commands for re-arming twice	Dennis Nicklaus	08/05/2016 03:10 PM
19601	Feature	Assigned	Normal	Firmware loading tool for Linux	Elliott McCrory	04/09/2018 03:26 PM
20552	Task	Work in progress	Normal	Channel level delays for IOTA BPM DAQ	John Diamond	08/07/2018 02:40 PM
20539	Task	Assigned	Normal	Verify ACNET devices and operation of IOTATG	John Diamond	08/06/2018 11:17 AM
20538	Task	Assigned	Normal	Create ACNET devices for IOTATG	John Diamond	08/06/2018 11:16 AM
20537	Task	Assigned	Normal	Configure startup parameters for IOTATG	John Diamond	08/06/2018 11:16 AM
20536	Task	Assigned	Normal	Create startup script for IOTATG	John Diamond	08/06/2018 11:15 AM
20535	Task	Assigned	Normal	Request ACNET node assignment for IOTATG	John Diamond	08/06/2018 11:14 AM
20534	Task	Work in progress	Normal	Request ethernet connection for IOTATG	John Diamond	08/06/2018 11:14 AM
20533	Milestone	New	Normal	Commission IOTATG	John Diamond	08/06/2018 11:17 AM
20439	Task	Work in progress	Normal	Implement DMA readouts for Raw and DSP data regions	John Diamond	07/25/2018 11:30 AM
20376	Task	Resolved	Normal	drvgen specification for IOTABPM250x8	John Diamond	07/23/2018 03:12 PM
20375	Milestone	Assigned	Normal	Device driver for IOTA DAQ module	John Diamond	07/25/2018 11:30 AM
20360	Task	Work in progress	Normal	drvgen specification for NML timing module	John Diamond	07/19/2018 02:42 PM
16713	Task	Accepted	Normal	Verify operating of ACNET devices and both timing modules	John Diamond	05/30/2017 11:30 AM
16710	Milestone	Assigned	Normal	Commission nmltrg	John Diamond	07/10/2017 10:39 AM
16537	Task	New	Normal	Create drvgen register map	John Diamond	05/15/2017 02:10 PM
12763	Bug	Accepted	Normal	Raw data device does not support channels	John Diamond	05/25/2016 12:17 PM
12754	Bug	New	Normal	slowness of init of 80MHz driver ??	John Diamond	05/23/2016 02:18 PM
12753	Bug	Resolved	Normal	80MHz board driver could be a little more forgiving of mistakes.	John Diamond	05/23/2016 02:38 PM
12390	Feature	Assigned	Normal	ACNET interface for CMTFInterlocksADC	John Diamond	04/26/2016 07:50 PM
11902	Milestone	New	Normal	Driver for VFRT-VUCD TSG	John Diamond	03/05/2016 10:01 PM
11895	Task	Assigned	Normal	DCCT ADC Driver	John Diamond	03/04/2016 10:28 AM
12769	Milestone	New	Normal	Develop a Prototype VME Driver Writer	Roger Tokarek	05/25/2016 03:49 PM
20359	Milestone	New	Normal	Driver for IOTA BPM Timing Module		07/18/2018 03:17 PM
16536	Milestone	New	Normal	Submit network connection request		05/15/2017 02:09 PM
16535	Milestone	New	Normal	Setup a test crate		05/15/2017 02:09 PM
16534	Milestone	New	Normal	Driver for 12-channel NML Timing Board		05/15/2017 02:10 PM
16345	Feature	New	Normal	Add offset to add into waveform points		04/26/2017 11:48 AM
13462	Bug	New	Normal	57 -97 errors on raw/triggered data channels > 6		08/05/2016 03:14 PM

#	Tracker	Status	Priority	Subject	Assignee	Updated
12389	Milestone	New	Normal	Driver for CMTF Interlocks ADC		04/26/2016 04:36 PM