

drvgen - Bug #20371

TSI-148 bridge does not support a master window with multiple data transfer modes

07/19/2018 03:59 PM - John Diamond

Status:	Assigned	Start date:	07/19/2018
Priority:	Normal	Due date:	
Assignee:	John Diamond	% Done:	100%
Category:		Estimated time:	2.00 hours
Target version:		Spent time:	0.25 hour
Description			
For the TSI-148 bridge chip, a call to vme_master_request fails when given VME_D8 VME_D16 VME_D32 as an argument for the data transfer attribute. A possible work-around is to replace with just VME_D32.			

History

#1 - 07/23/2018 11:07 AM - John Diamond

- % Done changed from 0 to 100

Changed to VME_D32. Maybe this should be something that can be configured in the header?