

Instrumentation Drivers - Task #20360

drvngen specification for NML timing module

07/18/2018 03:18 PM - John Diamond

Status:	Work in progress	Start date:	07/18/2018
Priority:	Normal	Due date:	
Assignee:	John Diamond	% Done:	20%
Category:		Estimated time:	2.00 hours
Target version:		Spent time:	2.00 hours
Description			
Should be almost identical to the Booster timing module spec.			

History

#1 - 07/19/2018 02:42 PM - John Diamond

- Status changed from New to Work in progress
- Assignee set to John Diamond
- % Done changed from 0 to 20

Not exactly identical to Booster timing module, not at all. More like the NML trigger board.

Had to divert attention to build support for the TSI-148 VME bridge into the Linux kernel to work with our test board. Found that some changes had to be made to the VME master window configuration in order to work with this chip. Verified that values are returned in network-byte order (just like with the Universe bridge).