

ANNIE1-electronics-design - Task #10457

Task # 10449 (Assigned): HV pickoff & signal splitter

Design and build full sized pickoff/splitter board

10/09/2015 03:15 PM - Jonathan Eisch

Status:	Assigned	Start date:	10/14/2015
Priority:	Normal	Due date:	01/08/2016
Assignee:	Jonathan Eisch	% Done:	100%
Category:		Estimated time:	0.00 hour
Target version:		Spent time:	0.00 hour
Description			
Subtasks:			
Task # 10458: panelize prototype design			Closed
Task # 10459: Verify prototype design with FNAL EE staff			Closed
Task # 10460: Produce full size pickoff/splitter board			Closed
Task # 10461: Full sized pickoff/splitter board population			Resolved
Task # 10462: Order components for full-sized pickoff/splitter board			Closed