

## ANNIE1-electronics-design - Task #10457

Task # 10449 (Assigned): HV pickoff & signal splitter

### Design and build full sized pickoff/splitter board

10/09/2015 03:15 PM - Jonathan Eisch

<b>Status:</b>	Assigned	<b>Start date:</b>	10/14/2015
<b>Priority:</b>	Normal	<b>Due date:</b>	01/08/2016
<b>Assignee:</b>	Jonathan Eisch	<b>% Done:</b>	100%
<b>Category:</b>		<b>Estimated time:</b>	0.00 hour
<b>Target version:</b>		<b>Spent time:</b>	0.00 hour
<b>Description</b>			
<b>Subtasks:</b>			
Task # 10458: panelize prototype design			<b>Closed</b>
Task # 10459: Verify prototype design with FNAL EE staff			<b>Closed</b>
Task # 10460: Produce full size pickoff/splitter board			<b>Closed</b>
Task # 10461: Full sized pickoff/splitter board population			<b>Resolved</b>
Task # 10462: Order components for full-sized pickoff/splitter board			<b>Closed</b>