

ANNIE1-electronics-design - Task #10456

Task # 10449 (Assigned): HV pickoff & signal splitter

Task # 10450 (Closed): Design and build prototype

Task # 10454 (Closed): Test prototype pickoff board

Test HV resilliance up to 3kV

10/09/2015 03:14 PM - Jonathan Eisch

Status:	Resolved	Start date:	10/09/2015
Priority:	Normal	Due date:	10/12/2015
Assignee:	Jonathan Eisch	% Done:	100%
Category:		Estimated time:	1.00 hour
Target version:		Spent time:	0.20 hour
Description			

History

#1 - 10/12/2015 02:40 PM - Jonathan Eisch

- Status changed from Assigned to Resolved

- % Done changed from 0 to 100

Tested fine (0ma) -2kV to +3kV for 1 minute each. No breakdown.