

Readout: 10 day plan, N-week plan

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23-Jan-2013

Wes

- SN mode, compressed. Reading across card and event boundaries with large (~1MB) DMA sizes. (On pace for) Writing at ~340 MB/sec with all 12 cards w.o. circular buffer (so will get perhaps faster). 16 cards will scale to 5 Gb/sec bus spec. This is ~4x too much data. Where's the lossy compression?
- After finishing the above test, he will use the circular buffer and ThreadConsumer to write to disk.
- Will then return to triggered mode to confirm 12 card, few Hz operation, not compressed.
- Can we increase time-out wait to a large value and relax the constraint that we not read across DMA boundary that so far we've been observing out of apparent necessity?
- Then reading compressed triggered data should work

Gennadiy

- Testing SN and trig mode simultaneously
- In compressed mode?
- If not, how is it working at all?

Georgia

- Consulting, Advising, Corrections!
- Testing FEMs

Outstanding issues that may require deeper level of engineering understanding than we have at FNAL: this is the N week stuff

- Can we get 0x ffff ffff event header to be some other value (0x dead beef), as these are commonplace in compressed running?
- What is reason for (partial) dropped event trailers, 0xe0000000.
- Trigger board quantities, and filling in Trigger data class data members for writing out.