SRF Cavity - ACCL:PIP2IT:H110 (on ioc2.fnal.gov)

FW Communication Diagnostics - ACCL:PIP2IT:H100 Rack A (on ioc2.fnal.gov)

LLRF Chassis Communication Diagnostics

Exit

SRF Cryomodule - ACCL:PIP2IT:H100 - Rack Hardware (on ioc2.fnal.gov)

SRF Cavity
Cryomodule ACCL:PIP2IT:H100

ACCL:PIP

ACCL:PIP2IT:H100 Hardware

Rack A

Rack Hardware Init and Test...

Resonance/Interlock... (RES) — Cavity 1, Cavity 2, Cavity 3, Cavity 4 — 30

Power Supply — 26

LO Distribution — 23

RF Station... (RFS1) — Cavity 1, Cavity 2 — 1.300e9 Hz — 19

RF Station... (RFS2) — Cavity 3, Cavity 4 — 1.300e9 Hz — 15

Precision Receiver Chassis... (PRC) — Cavity 1, Cavity 2, Cavity 3, Cavity 4 — 1.300e9 Hz — 11

LLRF Network

(RFS1) — Cavity 6 — 1.300e9 Hz — 19

RF Station... (RFS2) — Cavity 7, Cavity 8 — 1.300e9 Hz — 15

Precision Receiver Chassis... (PRC) — Cavity 5, Cavity 6, Cavity 7, Cavity 8 — 1.300e9 Hz — 11

EPICS<->LLRF Network

RF CPU | EPICS IOC

<SIOC:SYS0:AL00:M

<SIOC:SYS0:AL00:TOD>

RF Chassis Diagnostics - ACCL:PIP2IT:H100:RESA (on ioc2.fnal.gov)

RF Chassis
ACCL:PIP2IT:H100:RESA

RFS<->RES Comm.

Exit

Chassis Software Controller

State	Running
Last Error	
Count TX	363057
Count RX	363057
Count Timeout	0
Count Error	0
IP Address	resa

Reset

Halt

Chassis Monitoring

QF2 Board 6V	6.77 V
Kintex Temp	50.81 DegC
QF2 Board Temp	41.00 DegC

More FPGA Board...

Firmware

FW Code Hash: 5bda378b64d77c9cf77c10b418517d74452f8d28

<SIOC:SYS0:AL00:M

<SIOC:SYS0:AL00:TOD>

Cav 0 — Cav 1 — Permit

5BDA378B

DEA6B68E

Rev ID: 0

Counts: 0

Cav 1 Aux

Location: 2 2

Tx

Cav 0 Aux — Cav 1 Aux

Location: 0 0

<SIOC:SYS0:AL00:TOD>

0 0 At limit Tune Chim