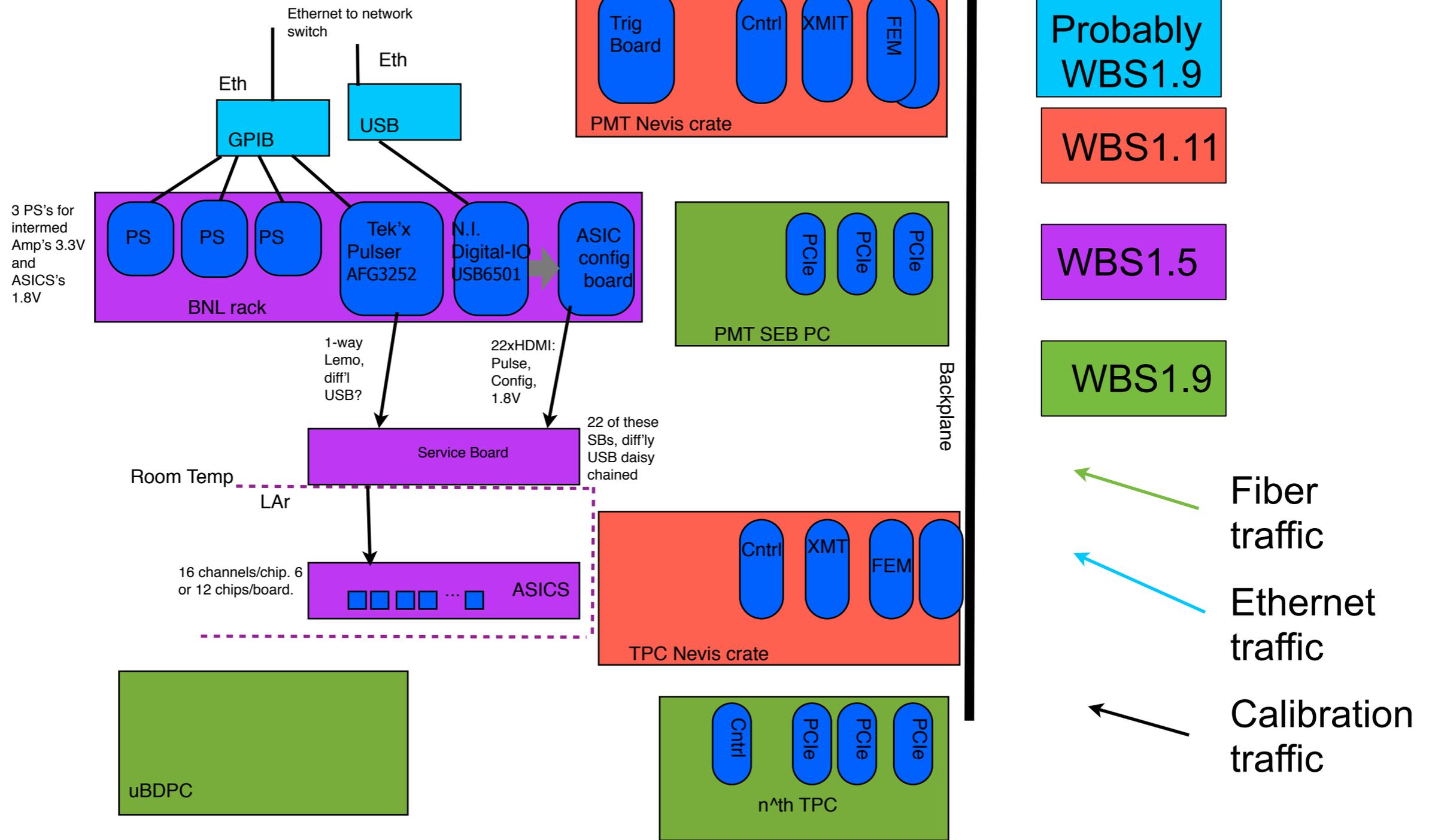
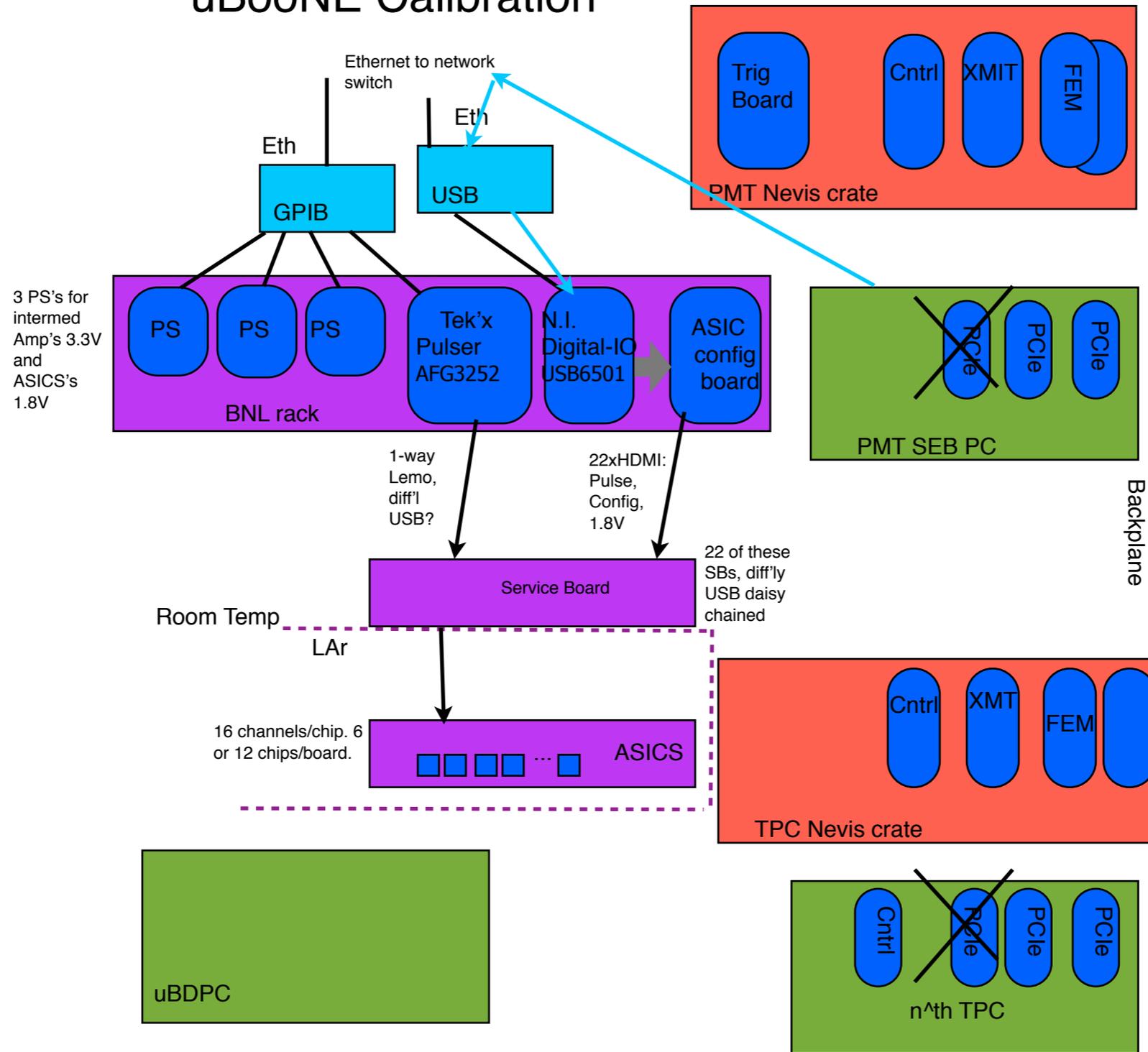


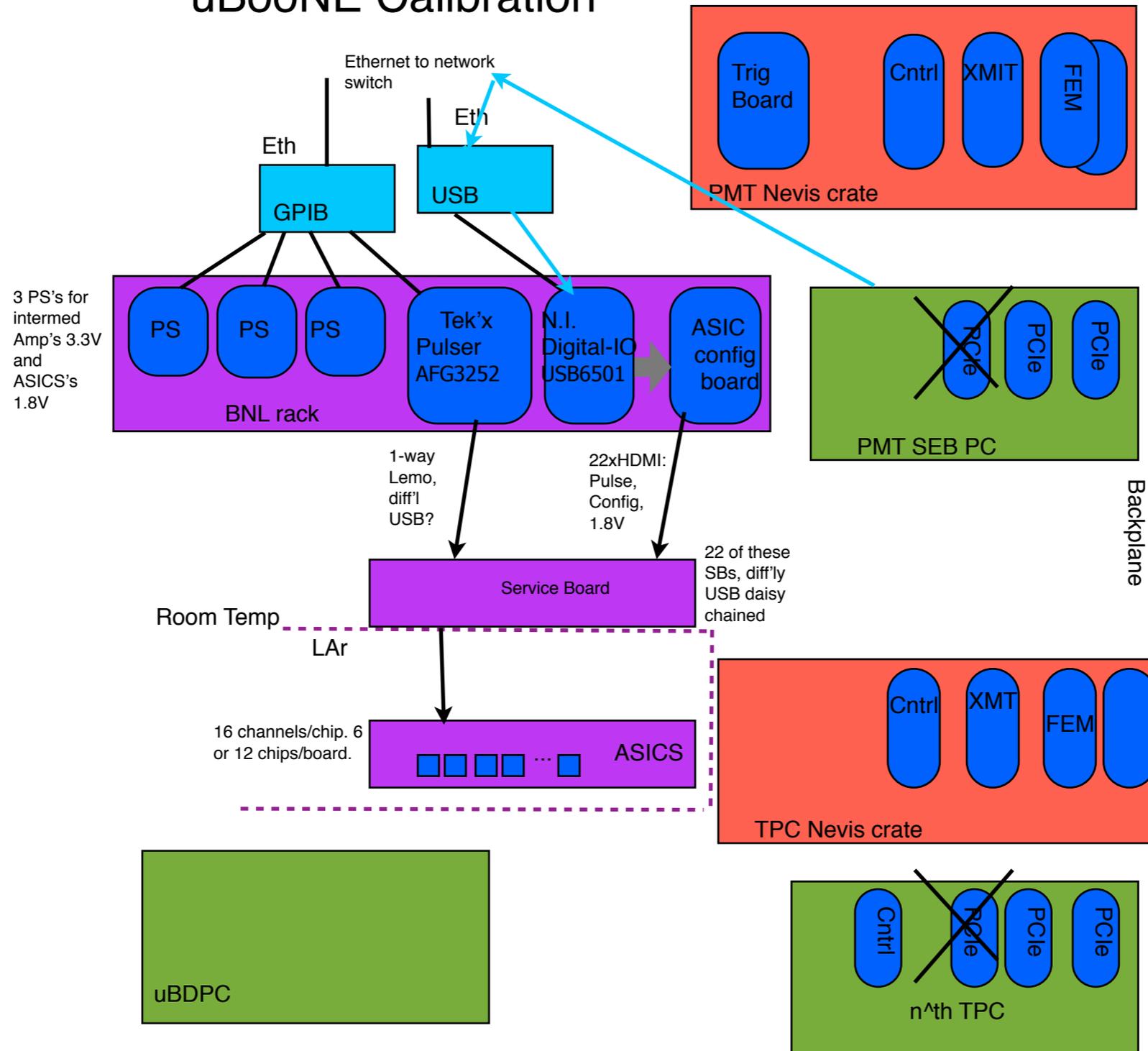
uBooNE Calibration



uBooNE Calibration

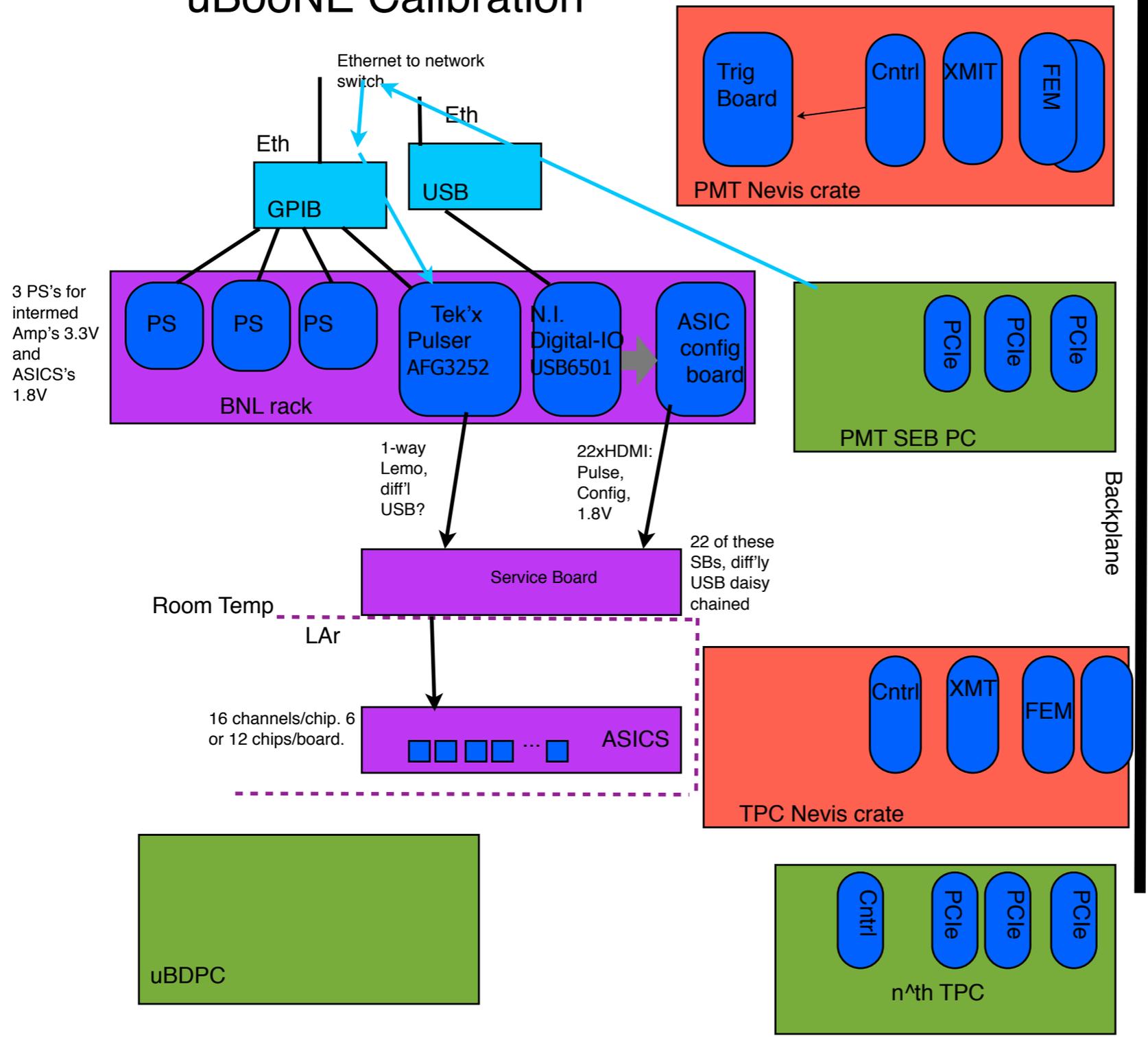


uBooNE Calibration

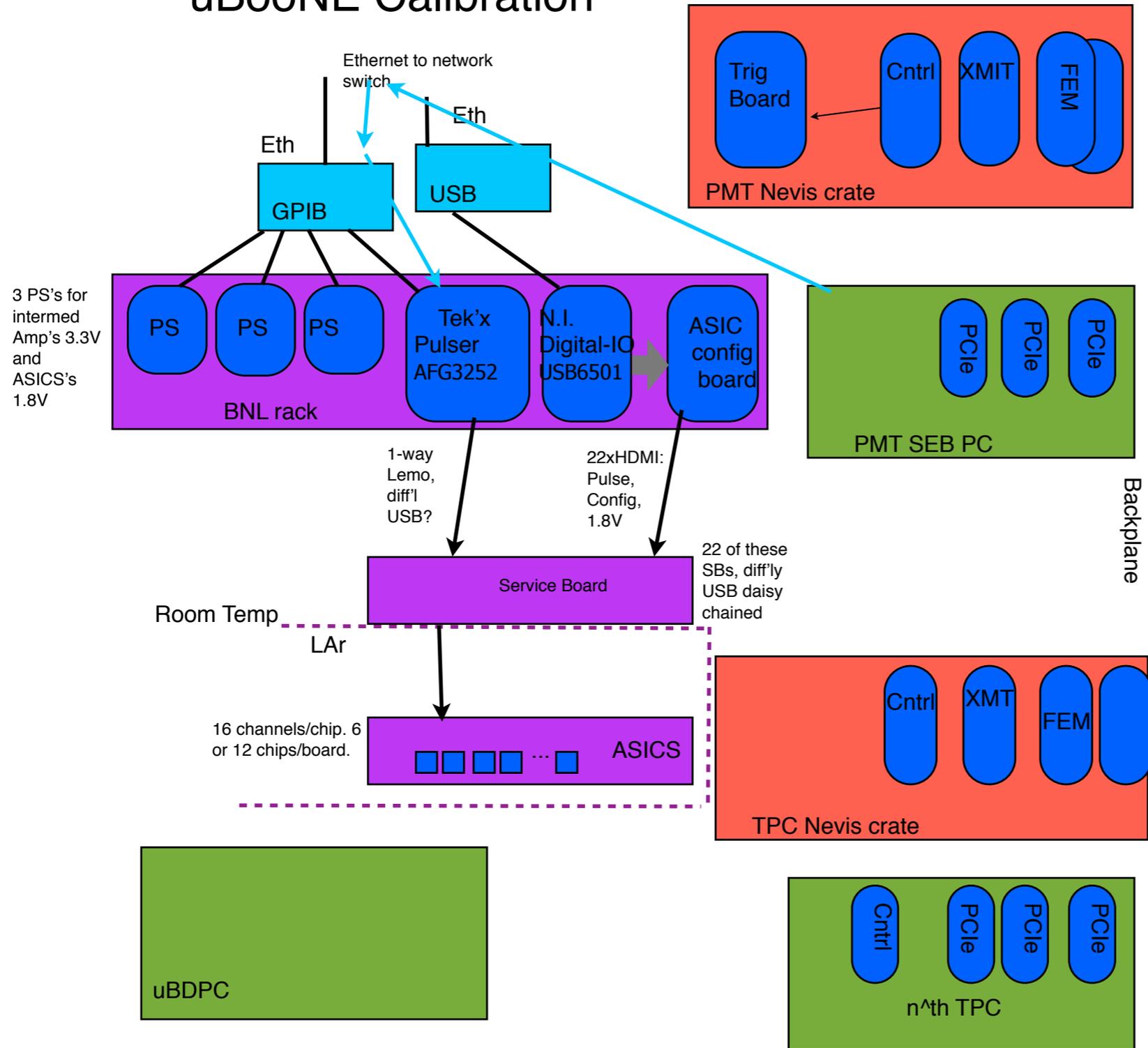


- 1.) uBDPC connects to all SEB PCs
- 2.) uBDPC tells PMTSEBPC we're doing a calibration run. Among other things, suspend the Supernova thread.
- 3.) PMTSEBPC configures ASICs through NI digital IO unit and ASIC config board, service board (peaking time, etc).
- 4.) PMTSEBPC configures Tektronix AFG3252 for pulse height, rise/fall time, pulse duration, trigger mode etc.
- 5-6.) Initialize, Setup TPCSEBPC Controller
- 7-8.) load SEBPC XMIT filepaths and parameters.
- 9-10.) load FEM FPGA, FEM parameters

uBooNE Calibration

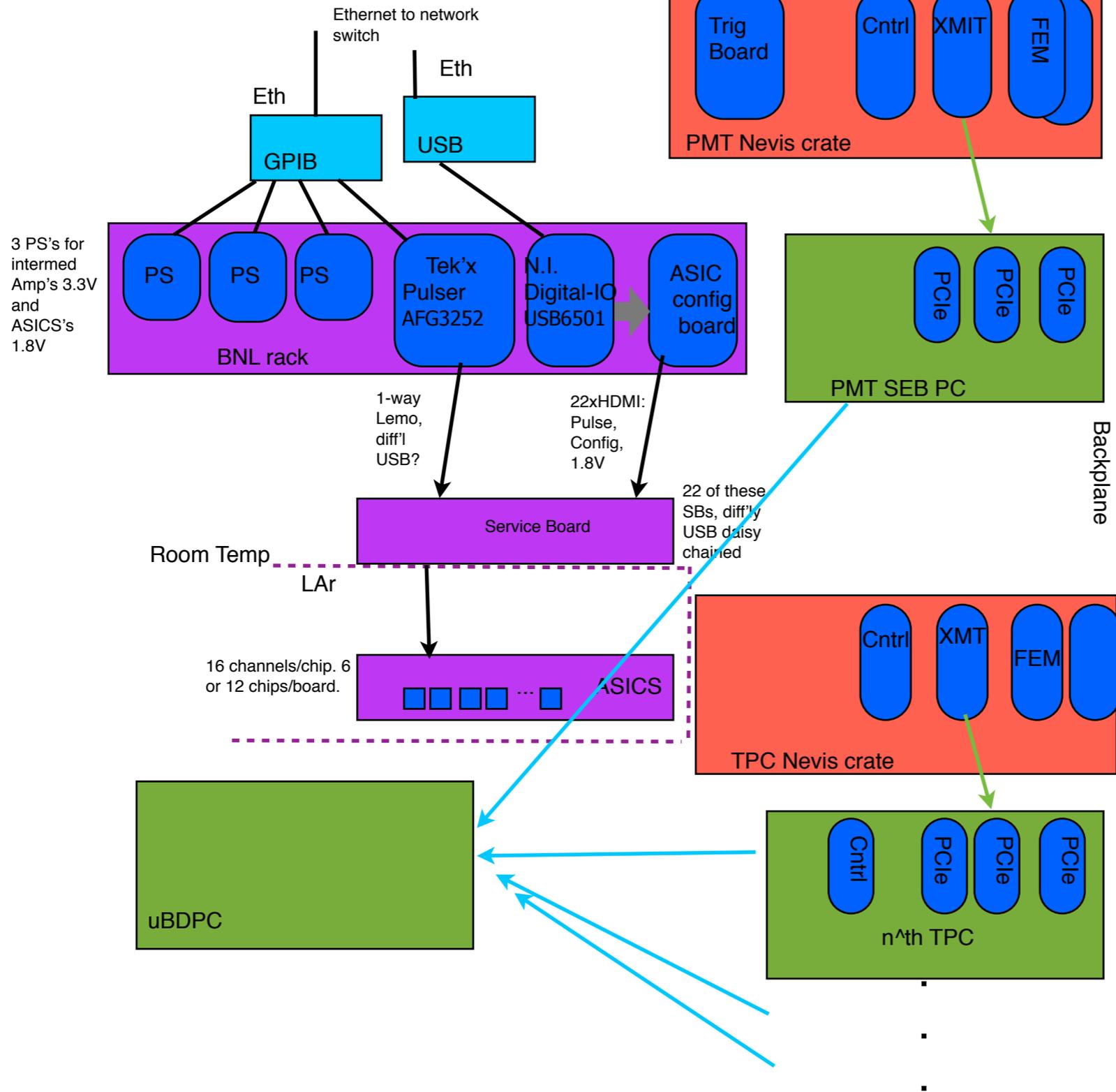


uBooNE Calibration

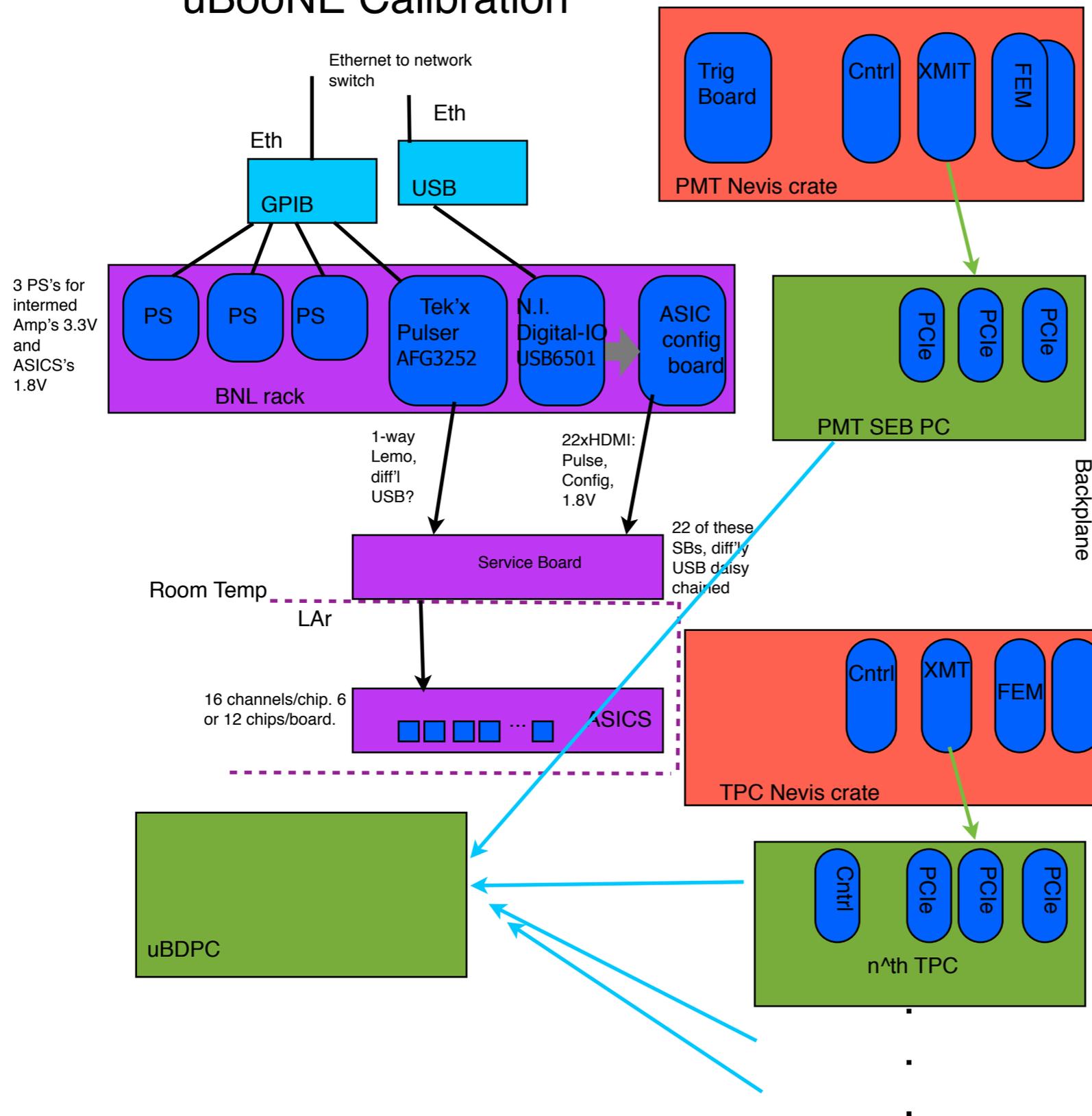


11.) setup XMIT PCIe DMA on all SEBPCs
 12-13.) PMTSEBPC tells Tektronix Pulser via Ethernet to fire calibration pulse (or via a LEMO/BNC cable from Trigger Board) ;
 Simultaneously, tell Trigger Board (via Cntrl Card) to set Calib Trigger bits. Perhaps also suppress all other triggers. This causes an event (3 x1.6 msec frames) at all crates.

uBooNE Calibration



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14.) SEBPCs reading status always, now see a triggered event.
 15.) This causes a DMA from XMIT into all the SEBPCs' memory via the PCIe dedicated to the triggered data stream.

The SEBPCs tell Assembler running on uBDPC they have data. Assembler sucks up those 4.8 msec of data from everybody. Compares PMTSEBPC's trigger info -- event number and full trigger word -- to event numbers of TPC/PMT data. Assembles event, writes it to disk.