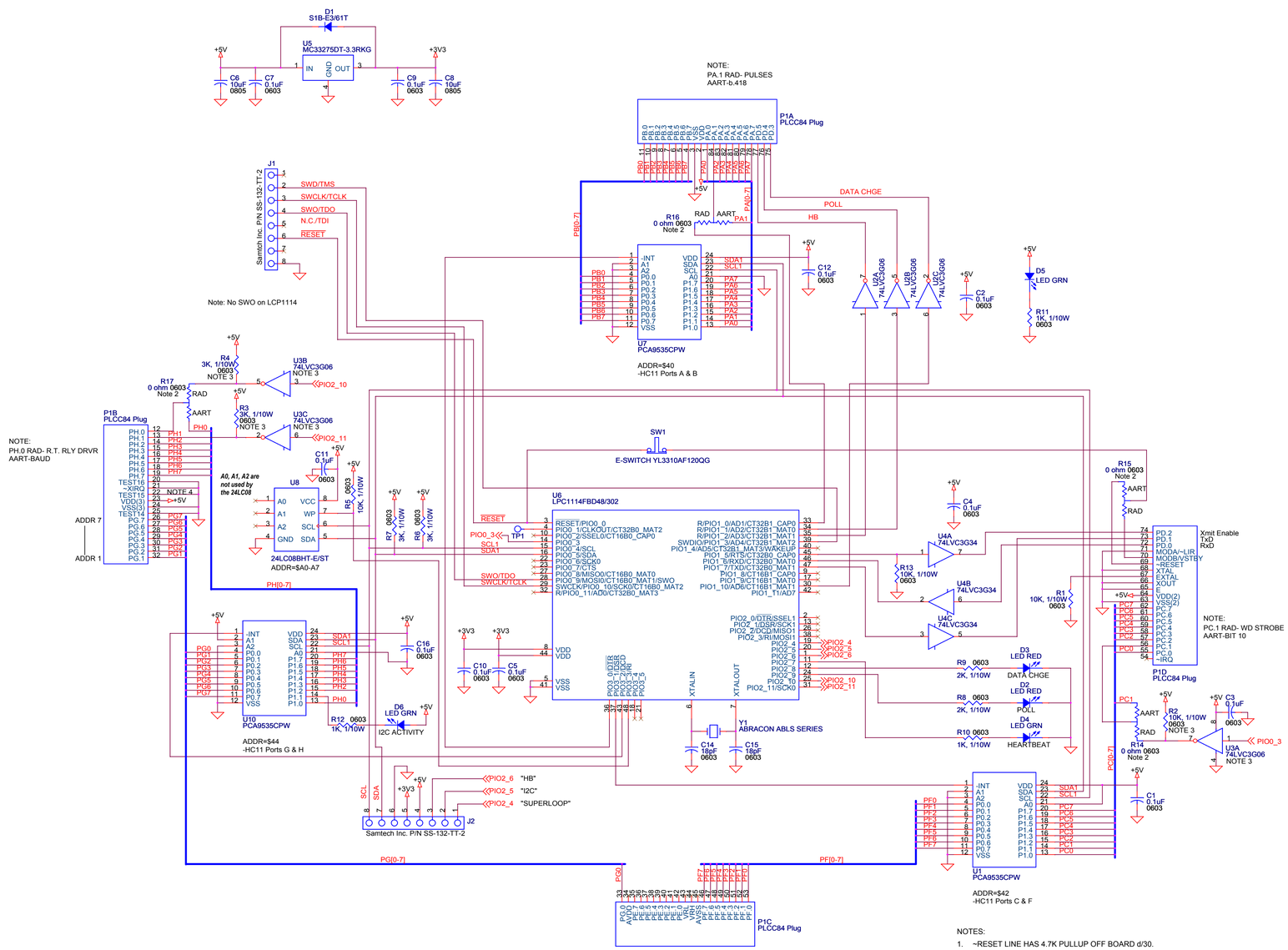
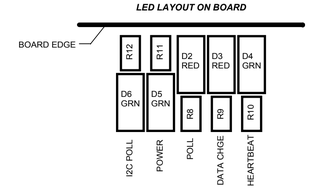
REV	DESCRIPTION	DRAWN / APPD	DATE
A	ADDED "I2C ACTIVITY" LED & RES TO U10-14; RAD/AART TRACE CONFIG TO P1-69 & 70. REMOVED U9 (74LVC3G06) FROM U6-11, 12 & 24; BYPASS C13; PWR FROM J1-1 &7. CHANGED R8 & R9 FROM 1K TO 2K; LEDS D2, D3 & D4 FROM +5V TO GND.	G.A.JOHNSON	10/04/2013
		G. GIESE	10/24/2013
B	ADDED 10K RESISTOR R13 TO U4 PIN 1. REPLACED "AART" & "RAD" PADS WITH 0 ohm RESISTORS R14, R15, R16 & R17. ADDED NOTE 2 AND LED LAYOUT.	G.A.JOHNSON	02/18/2014
		G. GIESE	02/28/2014
C	ADDED NOTES 3 and 4. ***NO CHANGES TO LAYOUT.***	G.A.JOHNSON	03/04/2016
		G. GIESE	03/04/2016



NOTES:

1. ~RESET LINE HAS 4.7K PULLUP OFF BOARD d/30.
2. FOR RESISTORS R14, R15, R16 AND R17, INSTALL RESISTOR ON "AART" SIDE FOR AART CARD APPLICATIONS AND INSTALL RESISTOR ON "RAD" SIDE FOR RAD CARD APPLICATIONS.
3. U3 IS A 74LVC3G06 IN AART APPLICATIONS. U3 IS A 74LVC3G34 IN RAD CARD APPLICATIONS. R2, R3 AND R4 ARE REMOVED IN RAD CARD APPLICATIONS.
4. PLUG PIN 22 (TEST15) MUST BE CUT IN RAD CARD APPLICATIONS.

LED LAYOUT ON BOARD



ITEM NO	PART NO	DESCRIPTION OR SIZE	QTY REQ

PARTS LIST

UNLESS OTHERWISE SPECIFIED

FRACTIONS	DECIMALS	ANGLES
+/-	+/-	+/-

1. BREAK ALL SHARP EDGES 1/64" MAX
2. DO NOT SCALE DWG
3. DIMENSIONING IN ACCORD WITH ANSI Y14.5 STD'S

MAX. ALL MACHINED SURFACES

ORIGINATOR	G. GIESE	02/15/2013
DRAWN	G. A. JOHNSON	02/15/2013
CHECKED	G. GIESE	07/03/2013
APPROVED	G. GIESE	07/03/2013
USED ON	PRINTED CIRCUIT BOARD 1310-BD-377763	
MATERIAL		

FERMI NATIONAL ACCELERATOR LABORATORY
UNITED STATES DEPARTMENT OF ENERGY

SAFETY SYSTEMS
MISCELLANEOUS
LPC 1114 MCU BOARD

SCALE	FILMED	DRAWING NUMBER	REV
		1310-ED-377762	C

Friday, March 04, 2016 — CREATED WITH ORCAD CAPTURE R15.7.0